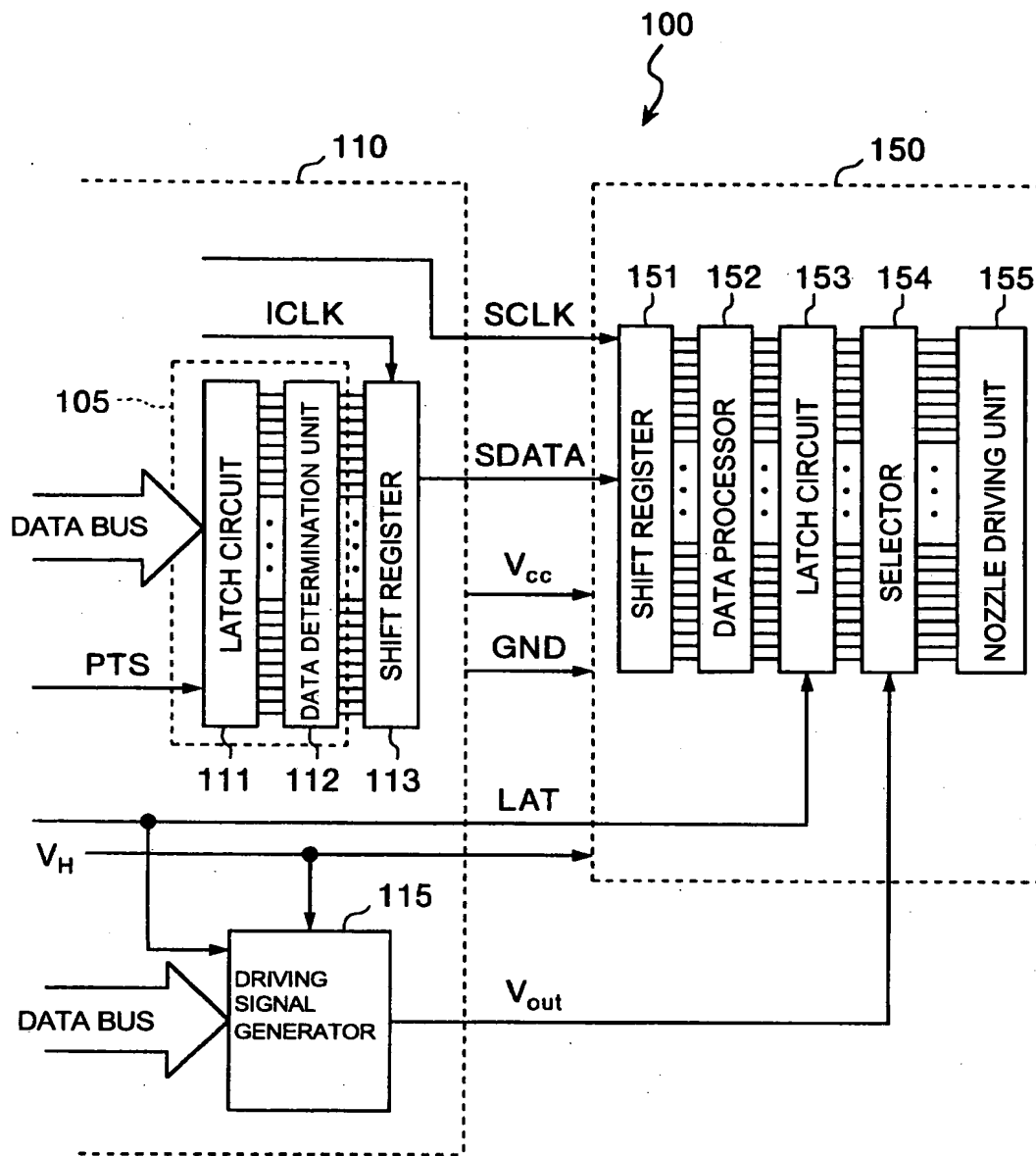


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FIG. 1



The block diagram illustrates the system architecture. A PC (200) is connected to a Driving Device (110) and a Head Unit (150). The PC (200) is connected to the Driving Device (110) via a PCI bus. The Driving Device (110) is connected to the Head Unit (150) via an FFC (Flexible Flat Cable).

[illegible]

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FIG. 3

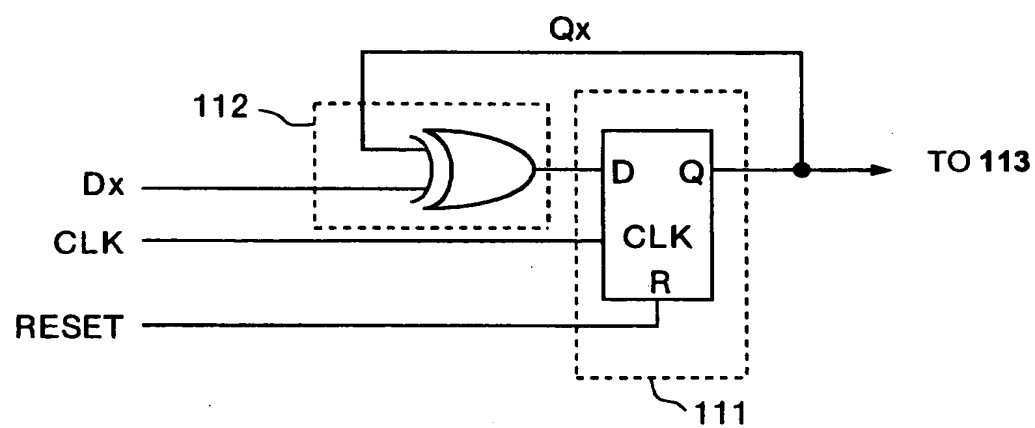


FIG. 4

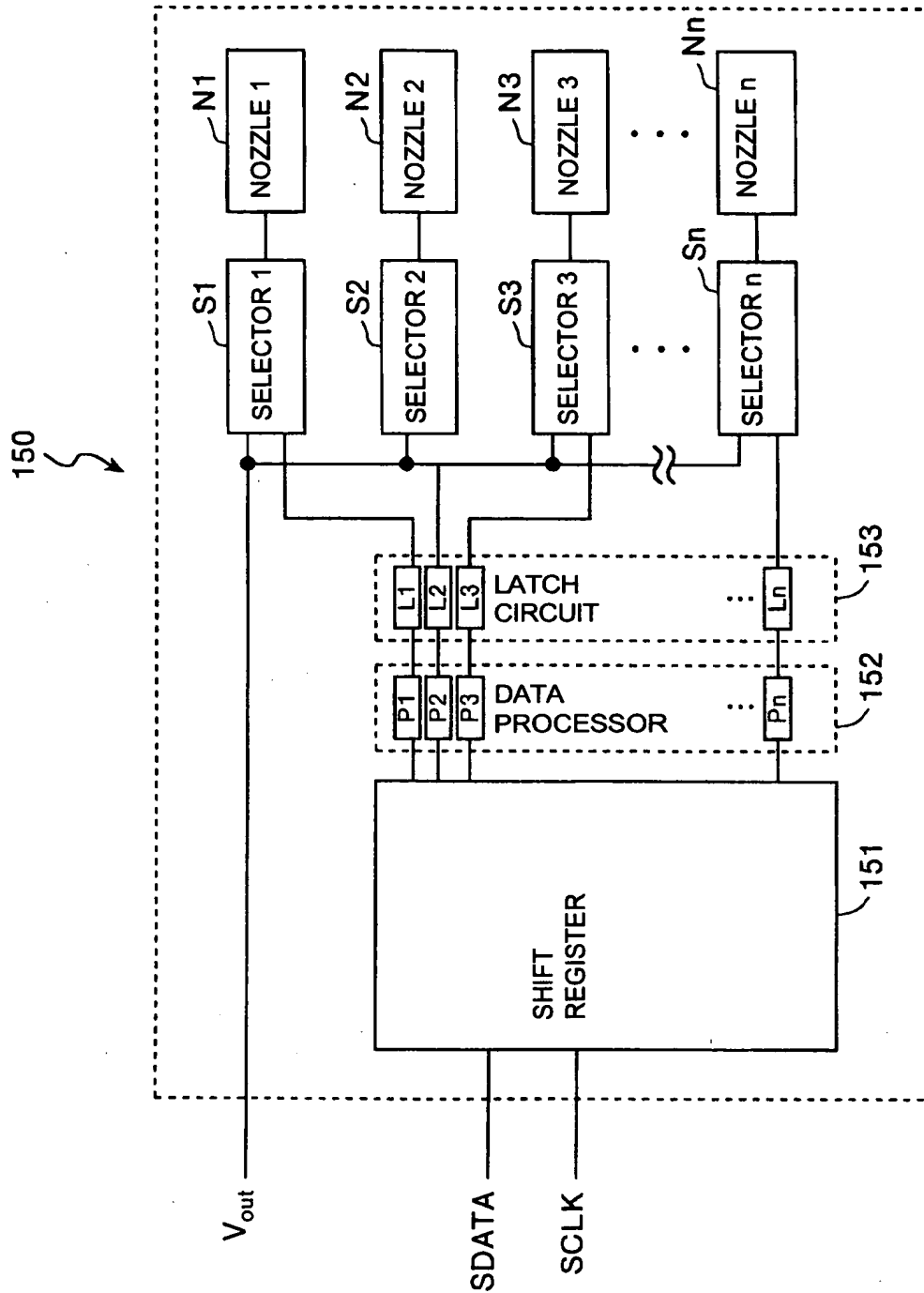
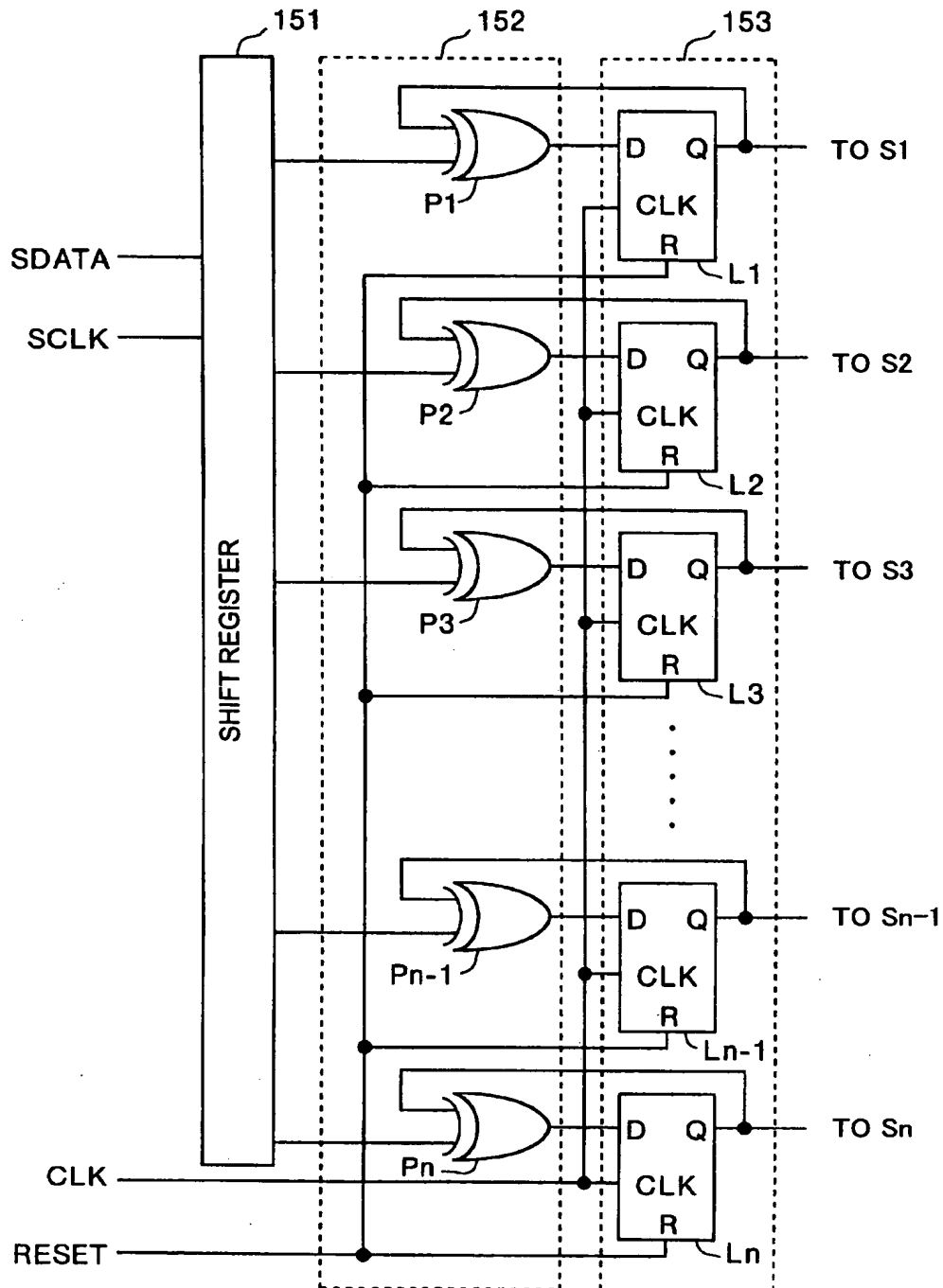
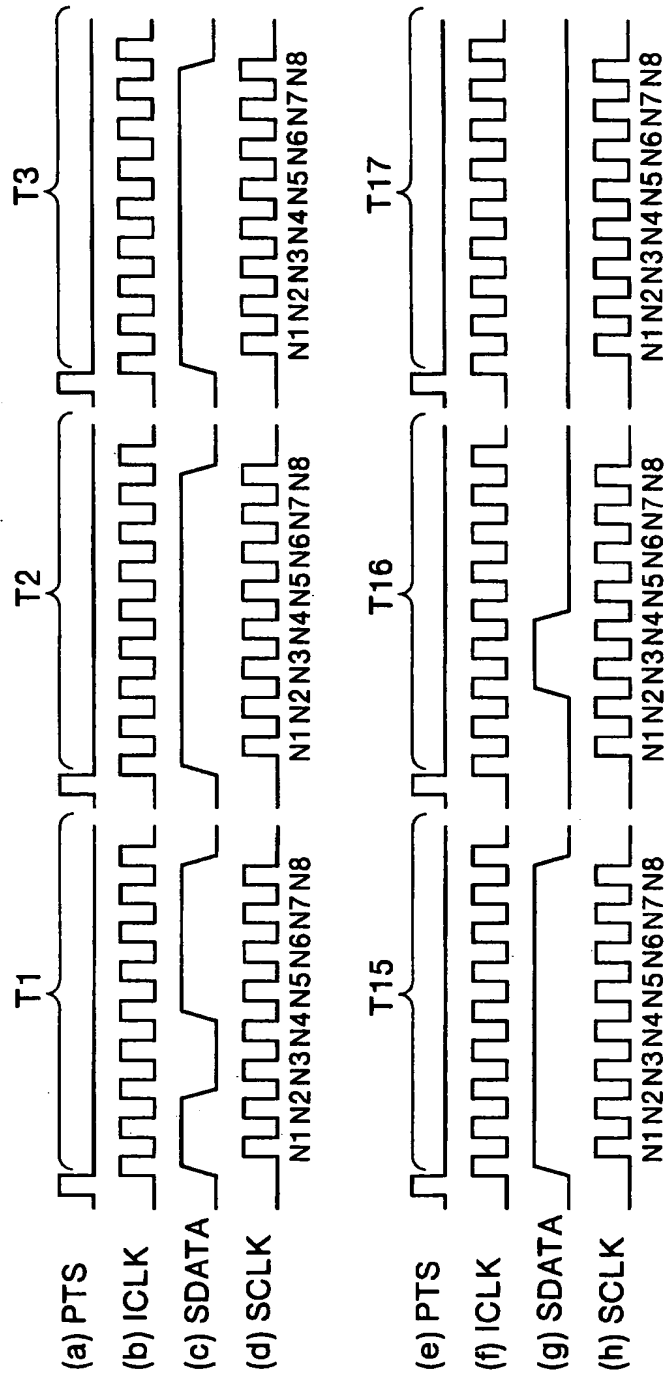


FIG. 5



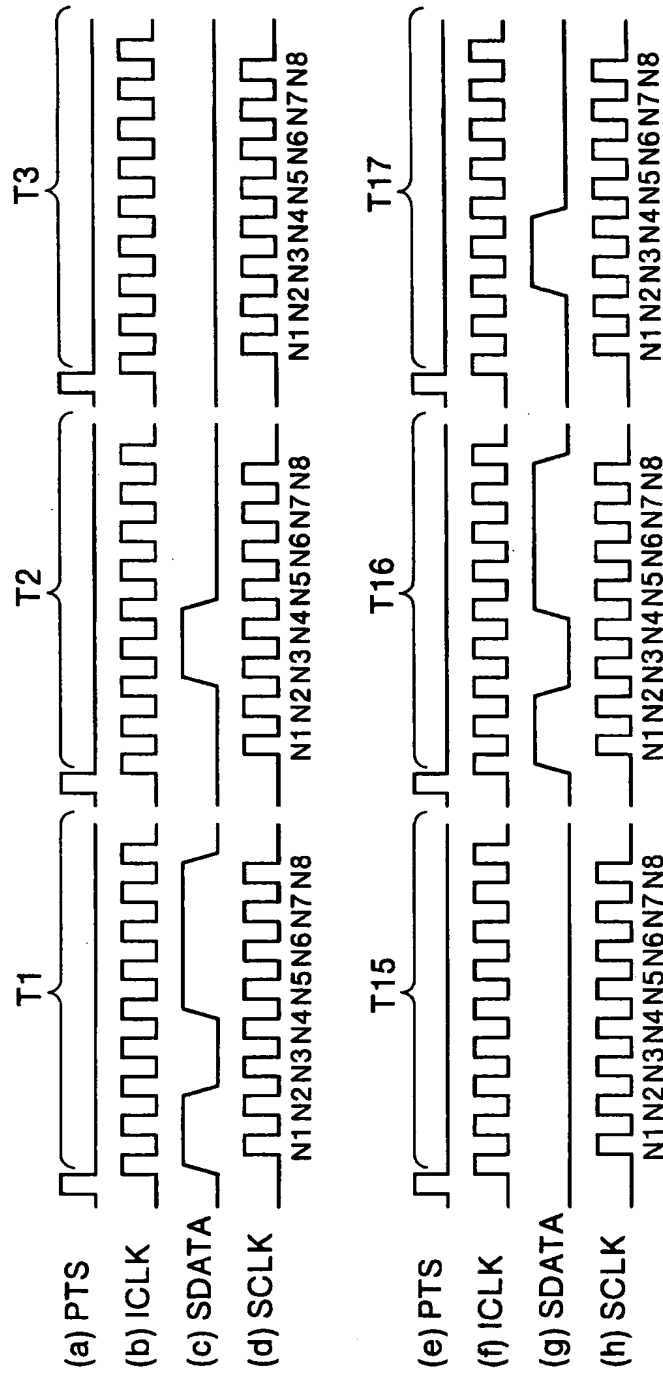
[illegible]

FIG. 7



PRIOR ART

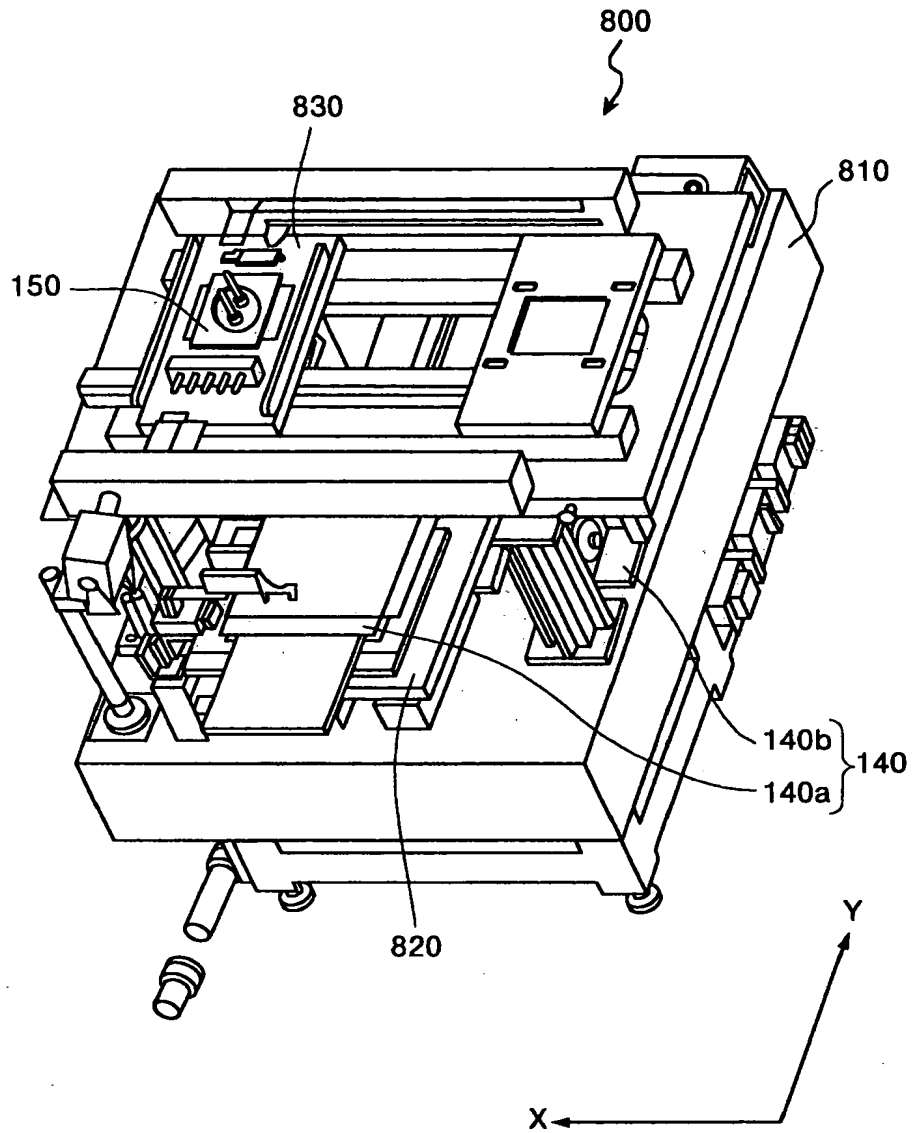
FIG. 8





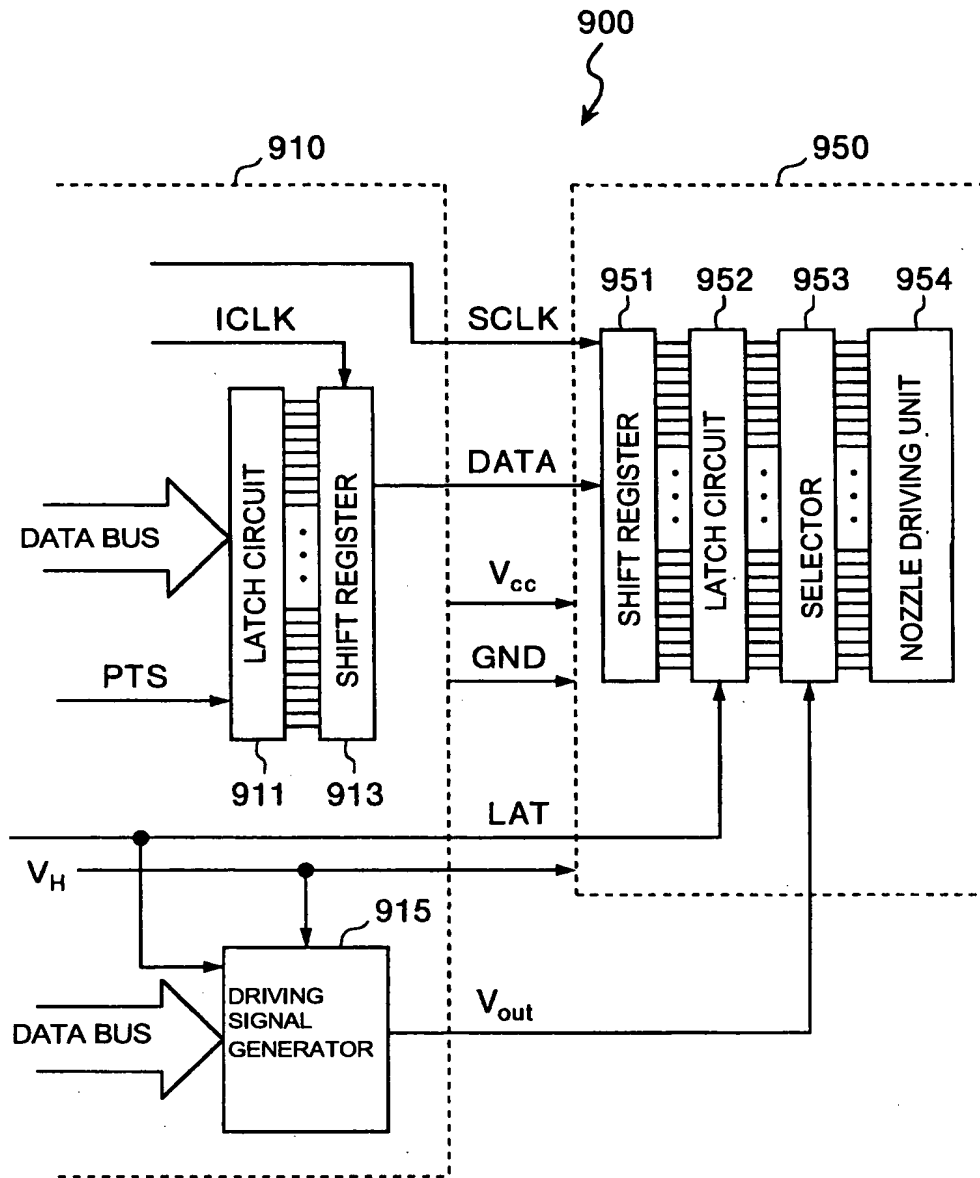
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FIG. 9



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FIG. 10



PRIOR ART